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**\*\* CONTINUING DATA \*\*\*\*\***  
 This application is a 371 of PCT/JP04/15835 10/26/2004 *SPS*

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***  
 JAPAN 2003-379981 11/10/2003 *SPS*

**\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***  
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35 USC 119(a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No <i>[Signature]</i> Examiner's Signature	Initials				

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**TITLE**  
 MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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